

DESCRIPTION OF THE COURSES

- Course code: ARR2112
- Course title: MICROPROCESSOR CONTROLLERS IN ENERGETICS
- Language of the lecturer: Polish

<i>Course form</i>	<i>Lecture</i>	<i>Classes</i>	<i>Laboratory</i>	<i>Project</i>	<i>Seminar</i>
<i>Number of hours/week*</i>	1		2		
<i>Number of hours/semester*</i>	15		30		
<i>Form of the course completion</i>	pass		pass		
ECTS credits					
Total Student's Workload					

- Level of the course (basic/advanced): basic
- Prerequisites:
completed courses: Fundamentals of Electronics
- Name, first name and degree of the lecturer/supervisor:
Janusz Staszewski, Ph. D.
- Names, first names and degrees of the team's members:
Marek Michalik, Ph. D.
Mirosław Łukowicz, Ph. D.
- Year:..... Semester:.....
- Type of the course (obligatory/optional): optional
- Aims of the course (effects of the course):
The course provides descriptions of basic architecture of microcontrollers, programming and some practical aspect of microcontroller application in engineering practice.
- Form of the teaching (traditional/e-learning): traditional
- Course description:
The course is aimed at explanation of practical problems encountered in microcontroller programming. On the basis of the 80C537 single-chip microcontroller the following topics are covered: basic aspects of microcontroller application in industry, microcontroller architecture and operation of basic blocks, instruction set, basic programming tools (assembler, linker, debugger), data transfer, addressing modes, arithmetic and logic operations, handling of port lines, A/C and C/A conversion, serial interface, LCD display, timers and counters. Each detailed course topic is an introduction to intensive laboratory exercise.
- Lecture:

<i>Particular lectures contents</i>	<i>Number of hours</i>
1. <i>Overview of one-chip microcontrollers. Architecture of micro-controllers 80C537. ALU, memory structure, addressing modes.</i>	2
2. <i>Fundamentals of microcontrollers programming. Handling of port</i>	2

<i>lines. Arithmetical and logical operations.</i>	2
3. <i>Timers and counters.</i>	2
4. <i>Interrupts.</i>	
5. <i>Code converting programs. Calculation of mathematical functions (square root, sine and cosine).</i>	2
6. <i>A/C and DC converters.</i>	2
7. <i>"Watchdog", serial interface.</i>	1
8. <i>Examples of microcontroller application.</i>	

- Classes – the contents:
- Seminars – the contents:
- Laboratory – the contents:
 1. Microcontroller laboratory set - use of programming tools (assembler, linker, debugger)
 2. Data transfer, addressing modes, simple arithmetic and logic operations
 3. Multibyte arithmetic operations
 4. Calculation of mathematical functions (square root, sine, cosine)
 5. Code converting programs
 6. Handling of port lines and keyboard
 7. Handling of programmable LCD alphanumeric display ‘2x16’
 8. Programming of timers and counters. Handling of interrupts.
 9. Time and frequency measurement
 10. Handling of A/C converter and implementation of C/A converter by use of PWM
 11. Serial interface and “watchdog”
 12. Practical realization of digital filter
- Project – the contents:
- Basic literature:
 - [1] “SAB 80C517/80C537 8-bit CMOS Single-Chip Microcontroller - User’s Manual”, Siemens AG, 1993
 - [2] Janiczek J., Stępień A., „Mikrokontrolery”, Wydawnictwo Centrum Kształcenia Praktycznego, Wrocław 1997.
 - [3] Janiczek J., Stępień A., „Mikrokontroler SAB 80(C)515/535”, Wydawnictwo Elektronicznych Zakładów Naukowych, Wrocław 1995
 - [4] Janiczek J., Stępień A., „Laboratorium Systemów Mikroprocesorowych cz. I”, Wydawnictwo Elektronicznych Zakładów Naukowych, Wrocław 1995
 - [5] Janiczek J., Stępień A., „Laboratorium Systemów Mikroprocesorowych cz. II”, Wydawnictwo Centrum Kształcenia Praktycznego, Wrocław 1996
- Additional literature:
 - [1] Starecki T., „Mikrokontrolery jednocukładowe rodziny 51”, Wydawnictwo NOZOMI, Warszawa 1996
 - [2] Szafran J., Wiszniewski A., „Algorytmy pomiarowe i decyzyjne cyfrowej automatyki elektroenergetycznej”, WNT, Warszawa, 2001
- Conditions of the course acceptance/creditation:

* - depending on a system of studies