

## DESCRIPTION OF THE COURSES

- Course code: ELR2167
- Course title: LOGIC DESIGN
- Language of the lecturer: Polish

<i>Course form</i>	<i>Lecture</i>	<i>Classes</i>	<i>Laboratory</i>	<i>Project</i>	<i>Seminar</i>
<i>Number of hours/week*</i>	<i>1</i>		<i>1</i>		
<i>Number of hours/semester*</i>	<i>11</i>		<i>11</i>		
<i>Form of the course completion</i>	<i>pass</i>		<i>pass</i>		
<i>ECTS credits</i>					
<i>Total Student's Workload</i>					

- Level of the course (basic/advanced): basic
- Prerequisites:  
completed course: Fundamentals of Electronics.
- Name, first name and degree of the lecturer/supervisor:  
Jan Iżykowski, Ph. D., D. Sc.
- Names, first names and degrees of the team's members:  
Eugeniusz Rosołowski, Prof., Ph. D., D. Sc.  
Janusz Staszewski, Ph. D.  
Mirosław Łukowicz, Ph. D.
- Year: Semester:
- Type of the course (obligatory/optional): optional
- Aims of the course (effects of the course):  
Gain of basic knowledge with respect to combinatorial and sequential logic circuits (automata). In particular, to be familiar with: - presenting the required operation of the logic circuit, - synthesis (design) and analysis of automata, - implementation issues. Relating both synthesis and analysis of the considered automata to practical cases of the automata application enables getting relevant skills for self-designing of the automata.
- Form of the teaching (traditional/e-learning): traditional
- Course description:  
The course consists of the lecture and laboratory, both dealing with the following problems. Boolean algebra and logic expressions. Methods of analysis and synthesis of combinatorial and sequential logic circuits (automata). Combinatorial logic circuits - simplification, designing and troubleshooting. Sequential automata: description and classification. Design of sequential automata with use of transition tables and output maps. Implementation of automata. Examples of the automata design.
- Lecture:

<i>Particular lectures contents</i>	<i>Number of hours</i>
<i>1. Introduction to Boolean algebra, logic expressions and laws. Logic gates and circuits.</i>	<i>1</i>

2. <i>Design of combinatorial logic circuits.</i>	2
3. <i>Sequential automata: types, general characteristic, description of automata.</i>	1
4. <i>Design rules for asynchronous sequential automata with use of transition tables and output maps.</i>	2
5. <i>Implementation of asynchronous sequential automata.</i>	1
6. <i>Examples of sequential automata design.</i>	2
7. <i>Pass test.</i>	2

- Classes – the contents:
- Seminars – the contents:
- Laboratory – the contents:
  1. Functional analysis of typical logic circuits.
  2. Design of asynchronous sequential logic circuits with use of transition tables and output maps – part I.
  3. Design of asynchronous sequential logic circuits with use of transition tables and output maps – part II.
  4. Design of synchronous sequential logic circuits.
- Project – the contents:
- Basic literature:
  - [1] Mano M. Morris, Digital design (second edition), Prentice-Hall Int., Inc., Englewood Cliffs, New Jersey, 1991.
  - [2] M. Morris Mano, C. R. Kime: Logic and computer design fundamentals, Pearson Prentice-hall Int., 2004, 3rd ed.
  - [3] Tocci R.J., Digital Systems. Principles and applications, Prentice-Hall Int., Inc., London, 1988.
- Additional literature:
  - [1] Układy logiczne. Ćwiczenia laboratoryjne. Skrypt Politechniki Wrocławskiej pod red. Mirosława Łukowicza. Oficyna Wydawnicza Politechniki Wrocławskiej, Wrocław, 2002.
  - [2] Wilkinson B., Układy cyfrowe. WKŁ, Warszawa, 2000.
  - [3] Skorupski A., Podstawy techniki cyfrowej. WKŁ, Warszawa, 2001.
  - [4] Kamionka-Mikuła H., Małysiak H., Pochopień B., Układy cyfrowe. Teoria i przykłady. Wydawnictwo Pracowni Komputerowej Jacka Skalmierskiego. Wydanie III poszerzone. Gliwice 2001.
  - [5] Majewski W., Układy logiczne. WNT, Warszawa, 1993.
- Conditions of the course acceptance/creditation:

\* - depending on a system of studies